



Docket No.: M4065.0646/P646  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Howard E. Rhodes

Application No.: 10/611,892

Confirmation No.:

Filed: July 3, 2003

Art Unit: N/A

For: OPTIMIZED TRANSISTOR FOR  
IMAGER DEVICE

Examiner: Not Yet Assigned

**INFORMATION DISCLOSURE STATEMENT (IDS)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned (37 C.F.R. § 1.97(b)(3)).

A copy of each reference on PTO/SB/08 is attached.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(a) exists. In accordance with

Application No.: 10/611,892

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37 C.F.R. § 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 C.F.R. § 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0646/P646. A duplicate copy of this paper is enclosed.

Dated: December 22, 2003

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Ryan H. Flax

Registration No.: 48,141

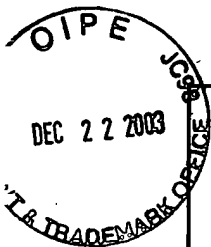
DICKSTEIN SHAPIRO MORIN &  
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant



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Substitute for form 1449A/B/PTO				<b>Complete if Known</b>	
				Application Number	10/611,892-Conf. #3670
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				Filing Date	July 3, 2003
				First Named Inventor	Howard E. Rhodes
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	M4065.0646/P646
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	AA	US-6,376,868	4/2002	Rhodes	
	AB	US-6,140,630	10/2000	Rhodes	
	AC	US-6,310,366	10/2001	Rhodes et al.	
	AD	US-6,326,652	12/2001	Rhodes	
	AE	US-6,204,524	3/2001	Rhodes	
	AF	US-6,333,205	12/2001	Rhodes	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	CA	Micron; Pixel Structure, Active Pixel Architectures; <a href="http://www.micron.com/imaging/Technology/Pixel_Structure/index.html">http://www.micron.com/imaging/Technology/Pixel_Structure/index.html</a> accessed 7/18/2002	
	CB	The Manufacturing Process; Chapter 2; fm page 33, Monday, September 4, 2000	
	CC	LLD Implant; <a href="http://www.cyberfab.net/training/processflow/ldd_implant.html">http://www.cyberfab.net/training/processflow/ldd_implant.html</a> Accessed 7/30/02	

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